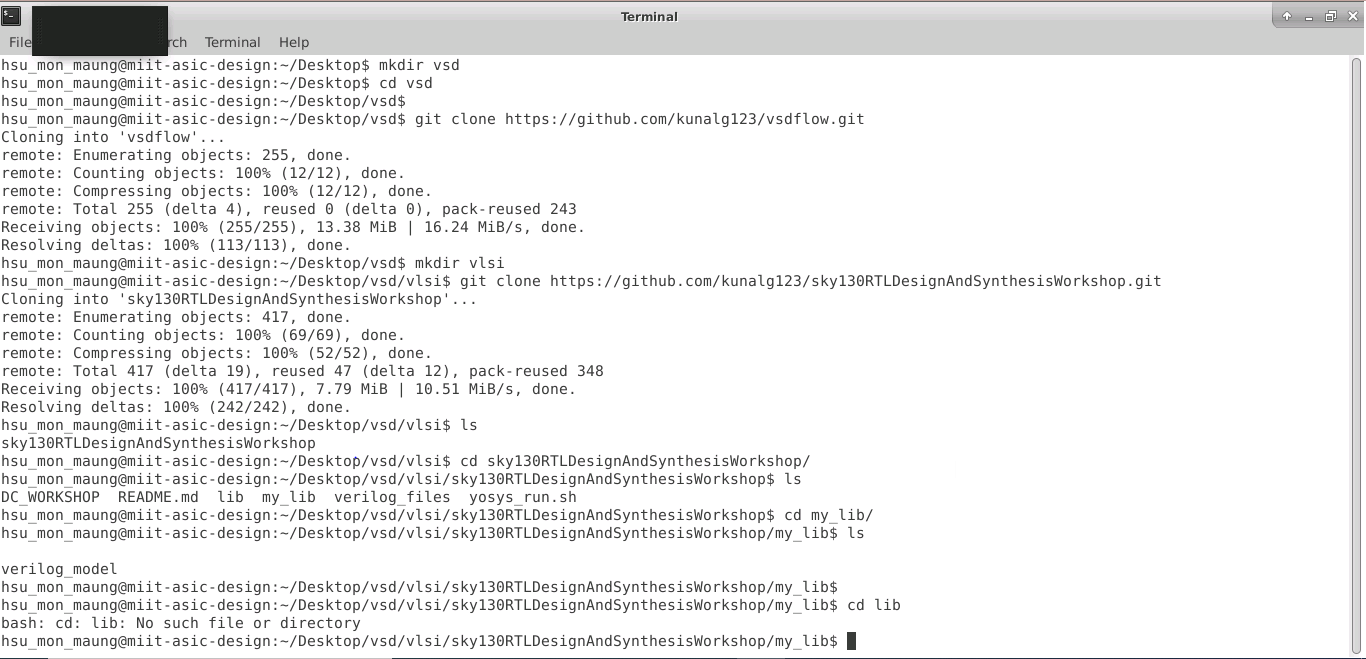
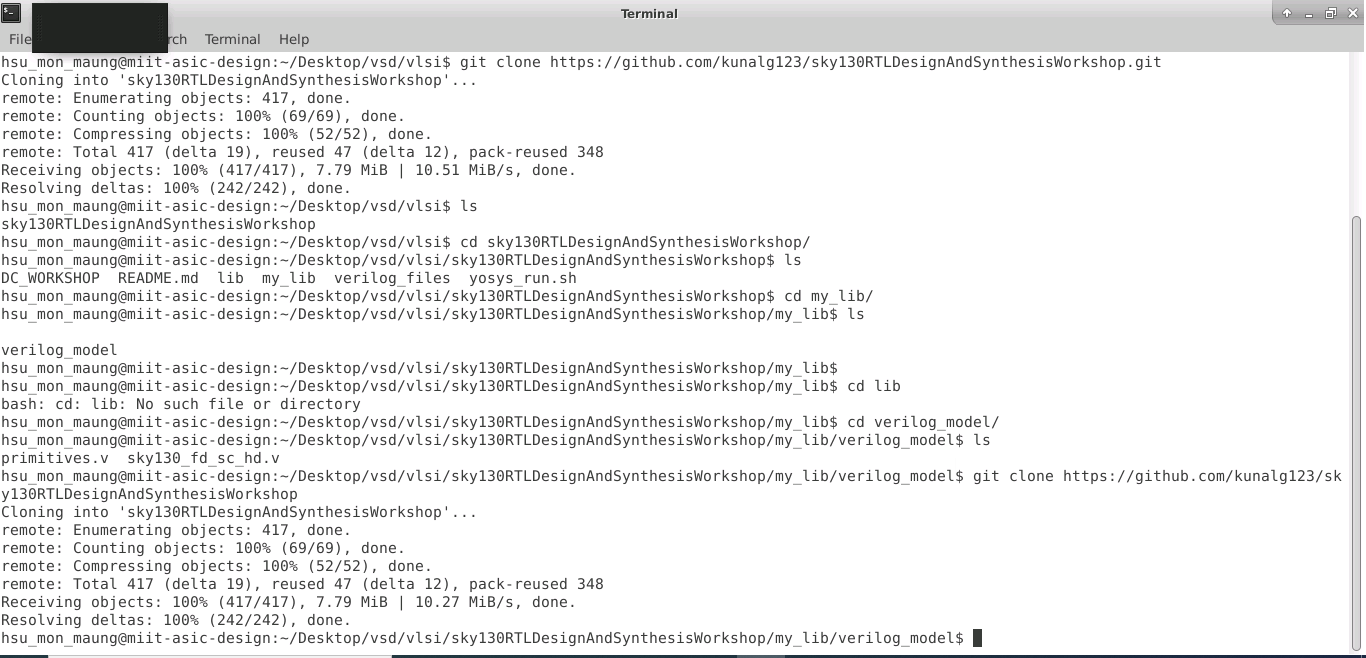


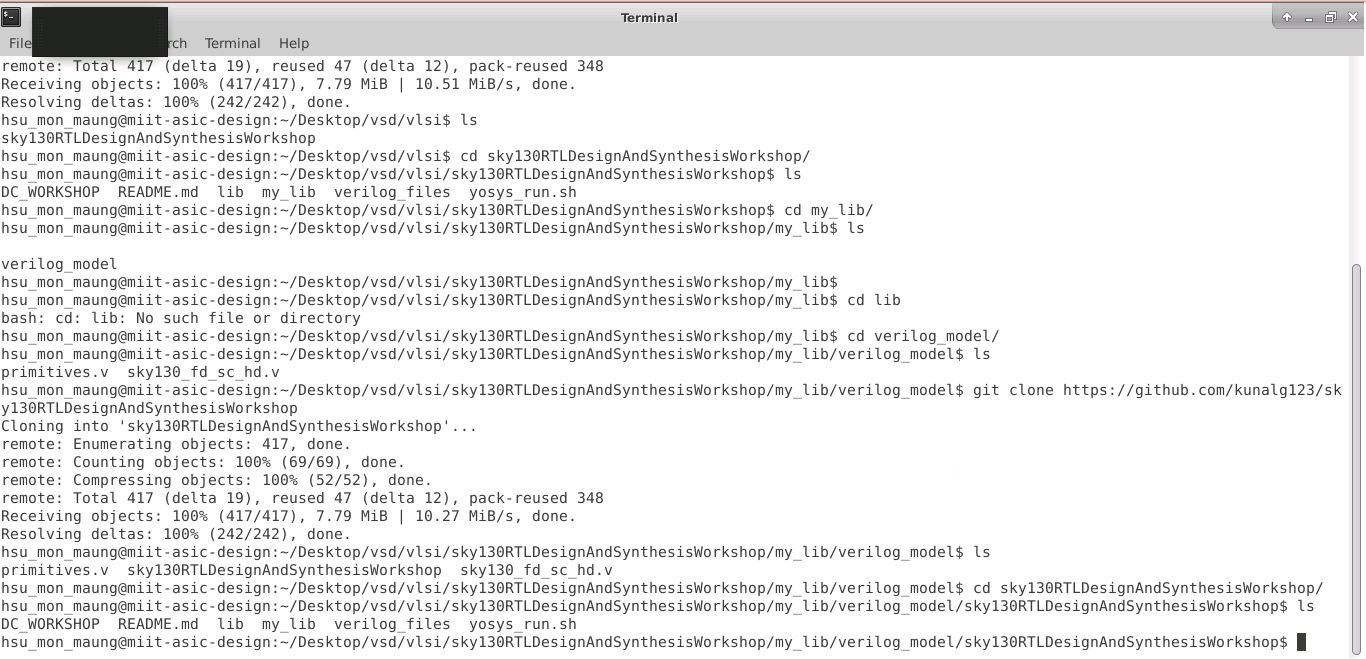
Below screenshot shows the above directory structure inside the vsd upto my\_lib directories that was set up through the terminal.



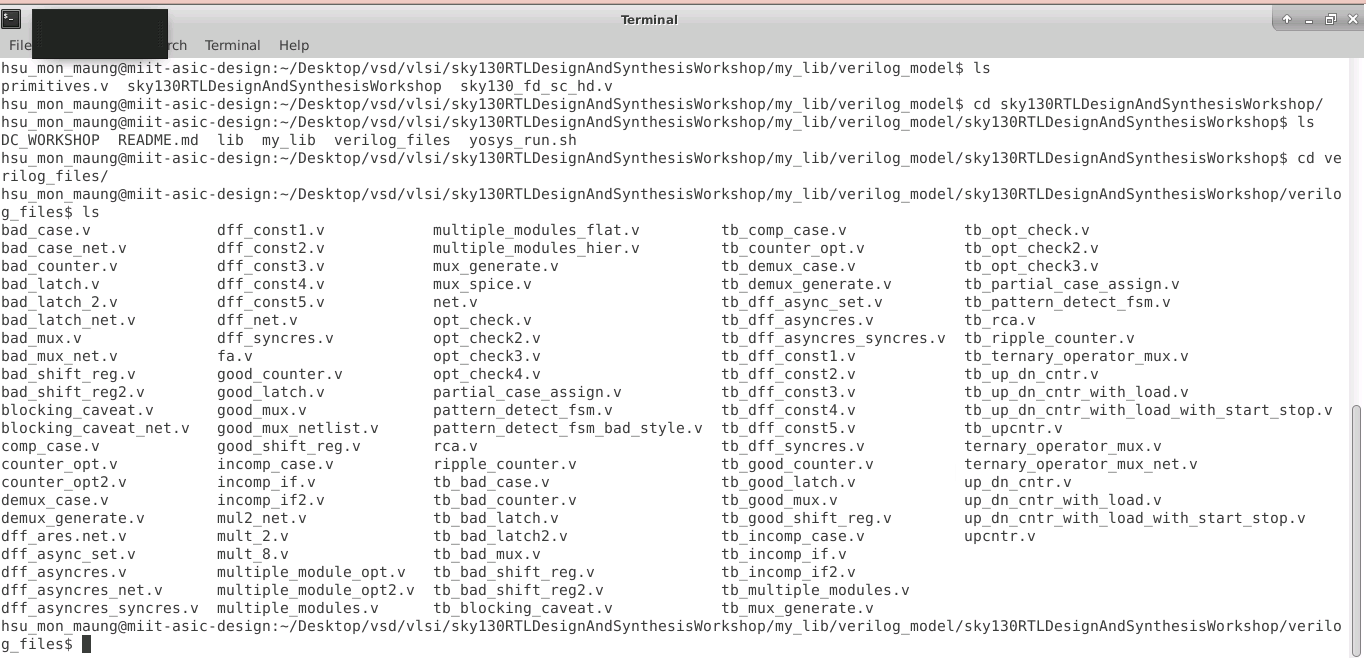
Below screenshot is git clone https://github.com/kunalg123/sky130RTLDesignAndSynthesisWorkshop



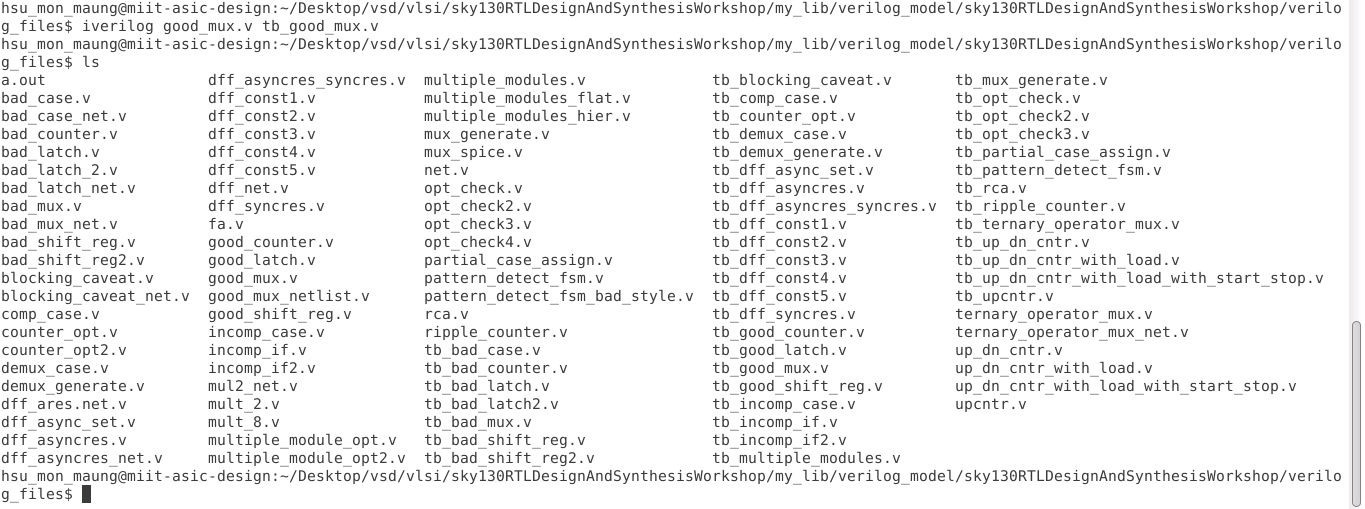
Below screenshot shows directory by cloning the sky130RTLDesignAndSynthesisWorkshop



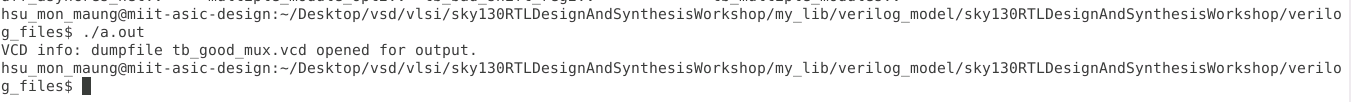
Below screenshot shows the list of verilog files.



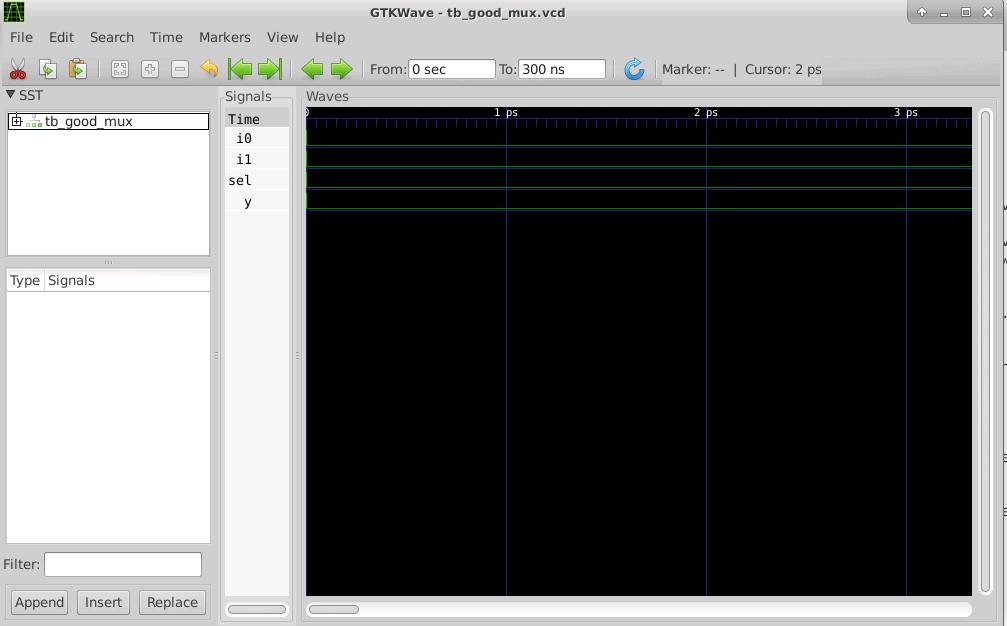
After this command - iverilog good\_mux.v tb\_good\_mux.v, following list of files can be seen

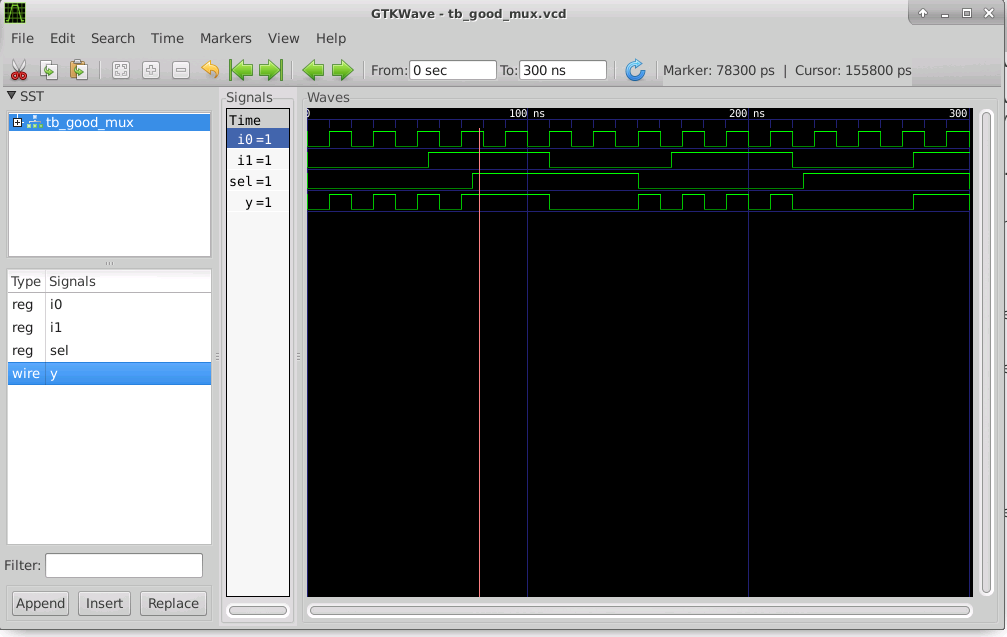


Below screenshot shows dumping the output into a vcd file by ./a.out

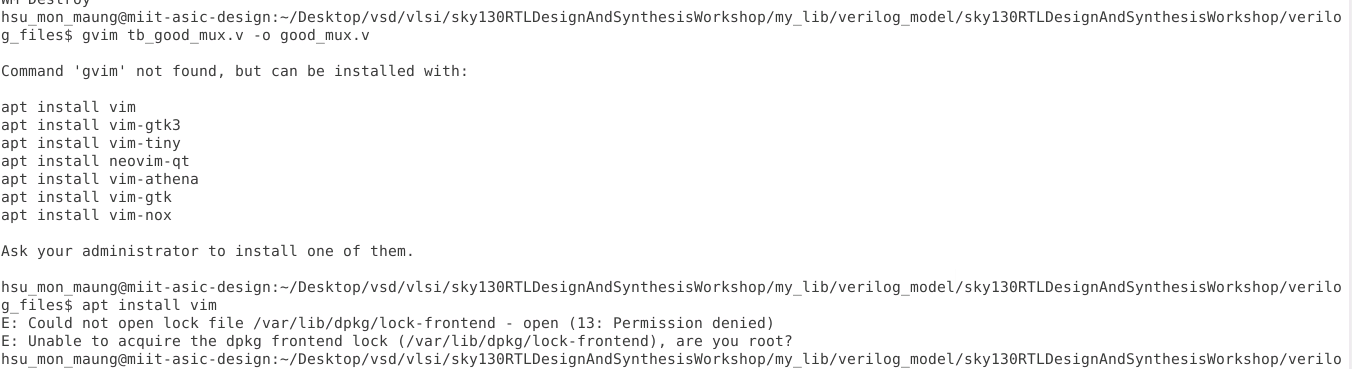


Below screenshot is gtkwave window.

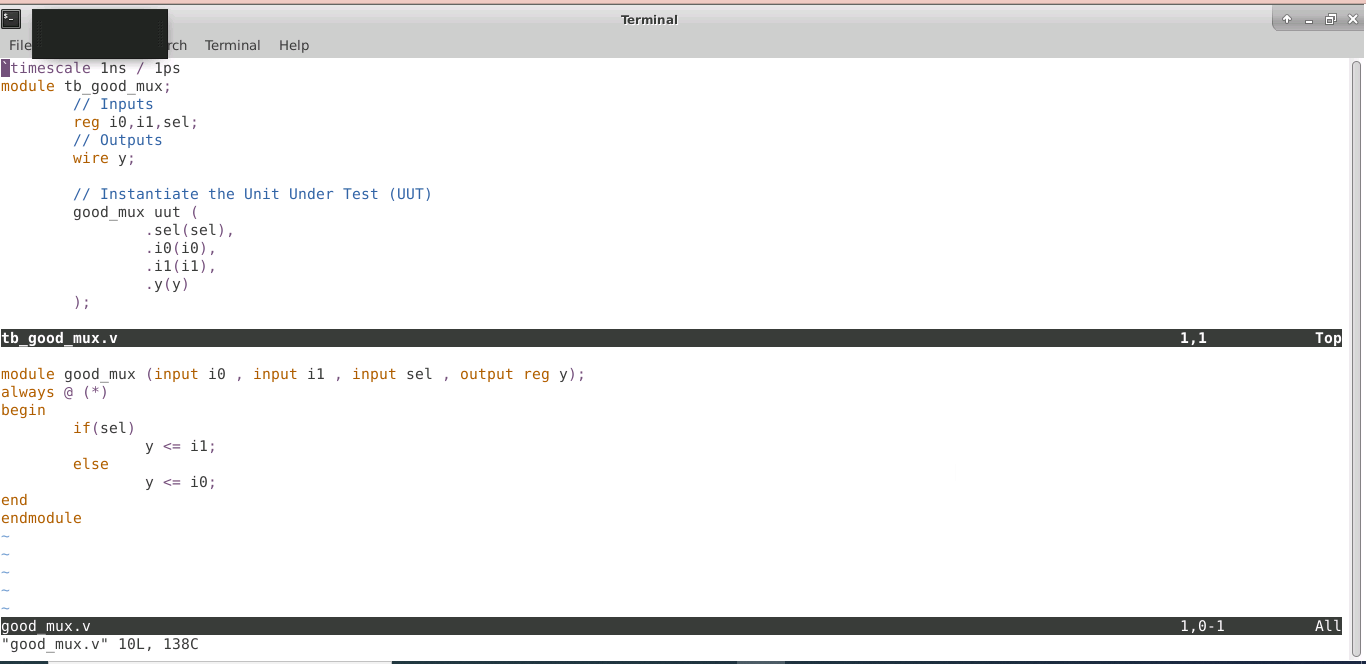




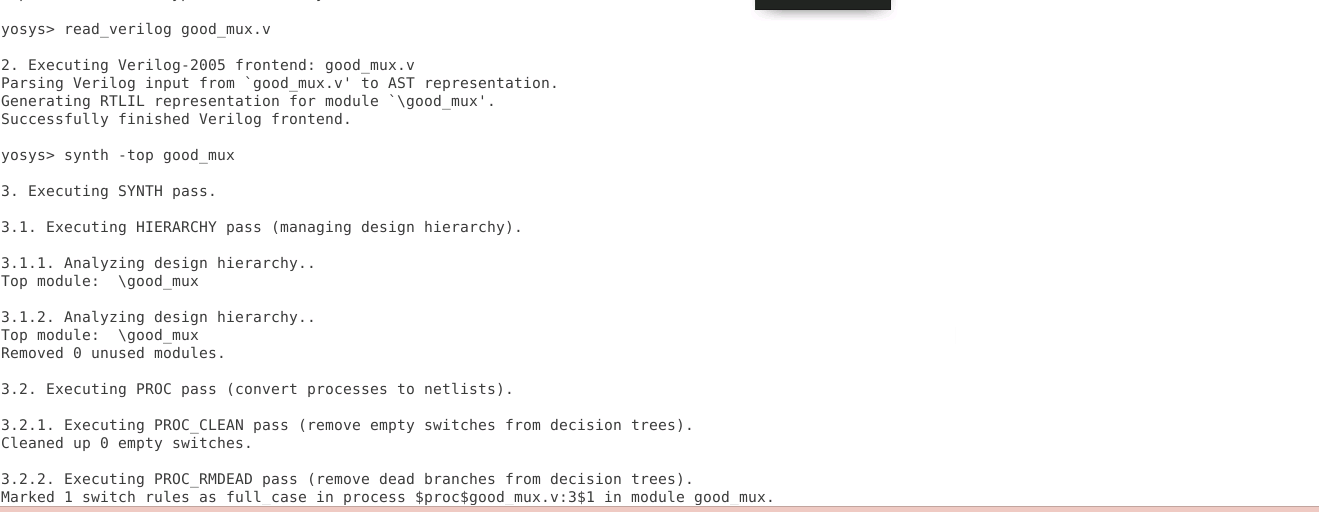
Error : gvim tb\_good\_mux.v -o good\_mux.v

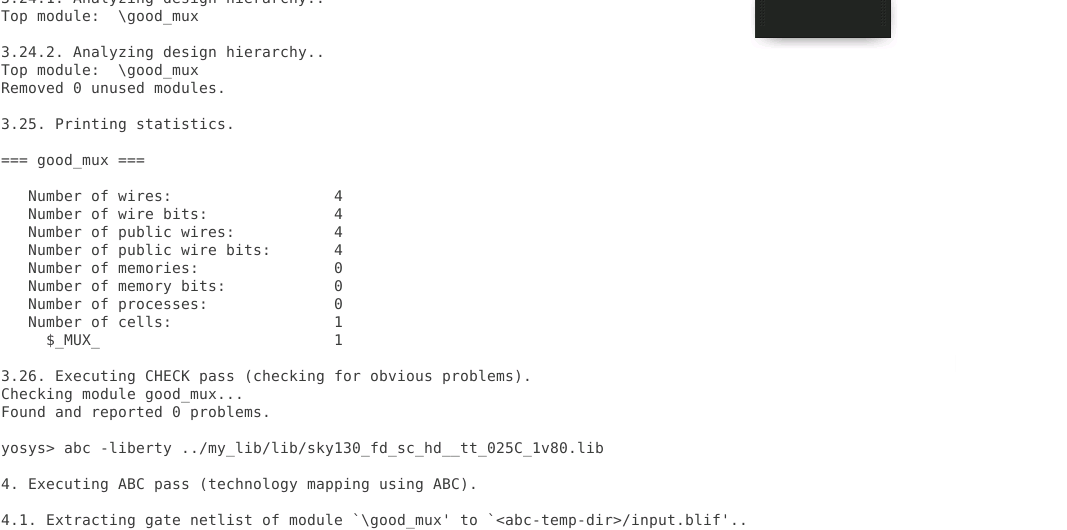


Below screenshot is vim tb\_good\_mux.v -o good\_mux.v

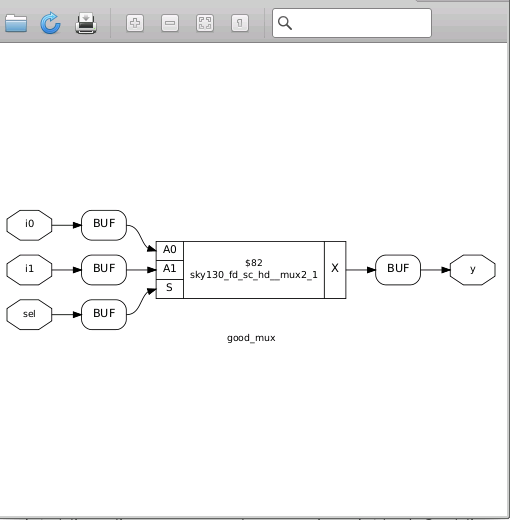


Below screenshot shows after synth -top good\_mux





Below screenshot is after show command.



Below screenshot is after commands : write\_verilog -noattr good\_mux\_netlist.v

!vim good mux\_netlist.v

